

## PRESS RELEASE

### 10<sup>th</sup> ANNUAL LINLEY PROCESSOR CONFERENCE FOCUSES ON PROCESSORS FOR COMMUNICATIONS, SERVERS, AUTOMOTIVE AND IOT

**MOUNTAIN VIEW, Calif. – September 14, 2016** – The 10<sup>th</sup> Annual Linley Processor Conference kicks off with a focus on the rapidly changing market dynamics affecting processors, according to The Linley Group, the industry's leading source for independent technology analysis of semiconductors for networking, communications, mobile, and servers.

The two-day conference will take place September 27 - 28 at the Hyatt Regency Hotel in Santa Clara, Calif., and features 22 technical presentations as well as panel discussions, sponsor exhibits, and networking lunches. Topics to be covered include processors for neural networks, vision processors, networking processors, server processors, high-speed memory, SoC interconnects, and processors for IoT gateways, IoT clients, and advanced automotive designs.

“Processors are becoming increasingly specialized to meet the needs of both high-volume and emerging applications, including embedded, networking, and IoT designs,” said Linley Gwennap, principal analyst and conference chairperson. “The Linley Processor Conference gathers leading processor vendors to deliver vetted presentations about their newest solutions. This year’s conference features eight new product announcements, including a new processor architecture for deep learning. These technical talks, plus two keynotes and a special panel session on enabling self-driving cars, give attendees the critical information they need to select the best processor technology for their designs.”

Products and technology being announced or disclosed at this event include:

**Wave Computing** – This presentation will introduce the details of the company's first Dataflow Processing Unit (DPU) architecture. Wave is the world’s first company to disclose a processor-chip architecture designed from scratch to accelerate deep learning.

**NetSpeed Systems** - This presentation introduces a new end-to-end coherency solution for high performance heterogenous SoC designs that supports CHI and ACE in a single design and uses advanced machine-learning algorithms to create a pre-verified cache-coherent interconnect.

**Mellanox** will debut its Innova Secure network adapter solution that offloads and accelerates security protocols and advanced network functions, enabling the ubiquitous use of encryption across the data center with the highest network throughput and superior server utilization.

**CEVA** - This presentation will introduce the latest CEVA vision processor and demonstrate how its neural-network software framework and unique “push button” network converter converts

pre-trained networks to real-time optimized networks for embedded devices, significantly reducing time-to-market.

**Cadence** – This talk will include details on some of the new products and features of the upcoming Cadence Tensilica announcement.

**ARM** - This talk will introduce ARM's next-generation coherent backplane IP that enables SoC architects to address these challenges using heterogeneous solutions that blend compute and acceleration.

**Andes Technology** - This presentation will introduce various grades of new embedded microprocessor cores, peripherals, and a bus fabric to support IoT SoC designs. Features, performance, benchmarks of new Andes IP cores will be presented.

**Adesto Technologies** - The presentation will discuss a new approach for XiP design, how it impacts the CPU's memory hierarchy and the non-volatile memory (NVM) device, compare it to traditional solutions and show how it contributes to an improved system design.

Presenting, panelist, and exhibiting companies include NXP, Synopsys, MediaTek, Cavium, InsideSecure, Cadence, CEVA, MoSys, IBM, NetSpeed Systems, Arteris, RISC-V, Mellanox, ARM, Wave Computing, Micron, Andes Technology, AppliedMicro, Adesto Technologies, EEMBC, RapidIO, VMware, and Quanergy Systems.

Attendees will have an opportunity to visit the sponsor's exhibits and network with industry leaders during the cocktail reception on Tuesday, September 27 at 5:00 p.m.

The Linley Group offers free attendance to qualified registrants who sign up by September 22. For more information on the conference program and to register, please visit [www.linleygroup.com/processor-conference](http://www.linleygroup.com/processor-conference).

### **About The Linley Group**

The Linley Group is the industry's leading source for independent technology analysis of semiconductors for networking, communications, mobile, and data-center applications. The company provides strategic consulting services, in-depth analytical reports, and conferences focused on advanced technologies for chip and system design. The Linley Group also publishes the weekly *Microprocessor Report*, *Networking Report*, and *Mobile Chip Report*. For insights on recent industry news, subscribe to the company's free email newsletters: *Linley Wire*, *Linley on Mobile* and *Processor Watch*.

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